

**GOVERNMENT OF TELANGANA
DEPARTMENT OF TECHNICAL EDUCATION**

**OFFICE OF THE
COMMISSIONER OF TECHNICAL EDUCATION
TELANGANA:: HYDERABAD**

Circular Memo

Sub: TECHNICAL EDUCATION – T & P Section - SRUJANA TECH FEST (STF) –
2024-25- State Level competitions – Instructions – Issued.

Ref: 1. Academic Calendar 2024-25.
2. This office Memo No: CTE-TP/TRNG/13/2025-TPS, Dt: 27.02.2025.

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SRUJANA TECH FEST, the annual science and technology festival of the Department of Technical Education, Telangana State has been conducted successfully at Institutional and district levels. Now the attention of all the Principals/OSDs of Government Polytechnics is invited to the 'STATE LEVEL SRUJANA TECH FEST (SLSTF)- 2024-25, which is the third level of Srujana Tech Fest.

1.0 Dates & Venues of SLSTF 2024-25:

The State Level Srujana Tech Fest should be conducted for each Circuit Branch (Clusters I to VI) on **18.03.2025** at the following Venues. (The grouping of all the branches of diploma courses into circuit branches (six clusters) is given in Annexure-I)

Cluster No.	Circuit Branch	Venue
Cluster-I	Civil Engineering & Allied Branches	639-GPW, Secunderabad
Cluster-II	Mechanical Engineering & Allied Branches	002-JNGPT, Ramanthapur
Cluster-III	Computer Engineering & Allied Branches	054-GIOE, Secunderabad
Cluster-IV	Electrical Engineering & Allied Branches	001-GPT, Hyderabad
Cluster-V	Electronics Engineering & Allied Branches	025-SDDGWTTI, Hyderabad
Cluster-VI	Special Branches	001-GPT, Hyderabad

However, the award function of the State Level Srujana Tech Fest will be conducted on **14.04.2025 (tentative)** along with Dr.B.R. Ambedkar meritorious awards function. The exact time, date and venue of the award function will be intimated later.

2.0 Organizing Committees:

There shall be a State Organizing Committee (SOC) to monitor the State Level Srujana Tech Fest 2024-25 event, consisting of the following officers:

1	Commissioner of Technical Education	Chairman
2	Secretary, SBTET, Hyderabad	Co-Chairman
3	Deputy Director(TECH-II), O/o CTE, Hyderabad	Members (Four)
	Deputy Director(TECH-I), O/o CTE, Hyderabad	
	Joint Secretary (Academic), SBTET, Hyderabad	
	Assistant Director / TPO, O/o CTE, Hyderabad	

There will be a core committee at the Institution level for conducting State Level Srujana Tech Fest, consisting of a State Level Coordinator and Assistant Coordinator for each Circuit Branch headed by the hosting Principal/OSD as the chairperson. The list of the State Level Coordinators and Assistant Coordinators for each Circuit Branch appointed by the Commissioner of Technical Education is given in Annexure-II. However, the Principal may form necessary other committees for smooth conduct of the event.

3.0 Cash Prize:

In order to encourage the young talented students, the 1st and 2nd prize winners at the State Level Srujana Tech Fest competitions will be given a cash prize of ₹25,000 and ₹18,000 respectively for the team (to be shared by six participating students) & ₹5,000 and ₹3,000 respectively for the mentors. The cash prizes will be given at the awards function of Srujana Tech Fest 2024-25.

4.0 Important Instructions for Evaluation of Projects:

- (i) The list of the projects selected for State Level Srujana Tech Fest 2024-25 as provided by the District Level Circuit Branch Coordinators, will be communicated to the State Level Circuit Branch Coordinators. On receiving the list, the State Level Circuit Branch Coordinators should prepare a list of participants in a prescribed format as mentioned in Annexure-III.
- (ii) Three (03) eminent personalities should be invited as judges for evaluation of projects, out of which at least one should be a subject expert from Universities (not less than the cadre of Associate Professor) and one from relevant Industries (not less than the cadre of Manager). For Cluster-VI (Special branches) one non engineering faculty should be invited as judge.
- (iii) The projects should be evaluated based on the parameters given in the evaluation sheet, which is in Annexure-IV.

- (iv) The evaluation process includes demonstration of the model and PPT presentation. Both should be done in the presence of all the participants for transparency and to acquire knowledge of other projects. Freedom should be given to the judges for free and fair evaluation.
- (v) On completion of evaluation of all the projects, a consolidated merit list should be prepared in a prescribed format given in Annexure-V by taking the average marks of all the judges.
- (vi) The State Level Coordinators for each Circuit Branch should prepare a list consisting of 1st and 2nd Prize winners (1st & 2nd rank scorers of the merit list) in the prescribed format as given in Annexure-VI and send scanned copies of Annexure-V & VI to adtra.ts@gmail.com & the original copies to CTE office through a messenger.
- (vii) The event should be completed at a stretch in a single day and the results should be declared on the same day in the presence of the judges and the participants.
- (viii) Written feedback should be obtained from the participants (as a group), mentors and judges, in the prescribed format as given in Annexure-VII and send the same to CTE office.

5.0 Instructions to the Participants and Mentors:

- (I) The participants should confirm their participation with the State Level Circuit Branch Coordinator of their respective branch well in advance. They should send the soft copy of the abstract of their Project along with two photographs of the clear view of the model at least three days prior to the event. An abstract is typically a standalone piece of writing that provides a brief overview of the entire project, allowing readers to quickly grasp the purpose, methodology, model and conclusions of the invention/project. It should not be more than a page of A4 size paper. It should be a word document typed in Times New Roman with 12pt font of 1.5 line spacing with justified alignment. Project title should be 14 pt bold font with centre alignment. The photographs of the model should not be more than 200 KB and should be in JPG format.
- (II) The participants should also submit two copies of Project Report in a spiral bound book, consisting of Title page, List of contents, Abstract, Acknowledgments, Introduction, Purpose, Design, Fabrication, Estimation of Cost etc.,

- (III) The participants should reach the venue by 9 AM positively on the date of State Level Srujana Tech Fest 2024-25 i.e., **18.03.2025**.

6.0 General Instructions to the organizers:

- (i) The Inaugural session should be completed by 10:30 AM positively and the criteria for evaluation (as mentioned in the Annexure-IV) should be explained to the participants clearly. Combined Inauguration may be conducted if competition is held at a venue for more than one cluster branches.
- (ii) The host Principal/OSD should make proper arrangements for the participating students. An amount of ₹75/per head may be collected from the participating Institution towards the working lunch, tea, snacks and water. Food may be prepared in Polytechnic attached hostels.
- (iii) Wide publicity of the event should be given through Print/Electronic /Social media.
- (iv) Invite SSC (10th class) students from nearby schools to this event. This is one way of promoting diploma education.

7.0 Expenditure:

- (i) The Principals/OSDs of the hosting institutions are hereby permitted to meet the expenditure incurred in conducting State Level Srujana Tech Fest 2024-25 in the following heads per each circuit branch from the schemes "Tech Fest and Awards" and "Industrial Visits" or "Institutional funds", If not, "Institutional accumulated funds". However the total expenditure per each circuit branch should not be more than Rupees Twenty Five Thousand (₹ 25,000).

1. Honorarium to Judges (₹3,000 X 3)	: ₹ 9, 000
2. Mementoes to dignitaries & Judges	: ₹ 2, 500
3. Bouquets to dignitaries & Judges	: ₹ 1, 500
4. Banners	: ₹ 1, 500
5. Lunch/Tea/snacks for guests/judges/organizers	: ₹ 3, 000
6. Certificates for the Participants	: ₹ 6, 000

	₹ 23, 500

- (II) The Principals/OSDs of the participating Institutions are hereby permitted to meet the expenditure towards the transport (Project model along with students and mentor) and working lunch for students (₹75/per head) from the schemes "Tech Fest and Awards" and "Industrial Visits" or "Institutional funds", If not, "Institutional accumulated funds". Public transport should be utilized as far as possible for transportation.

The receipt of the Circular memo shall be acknowledged.

Encl: Annexures I to VII

Sd/-A. Sridevasena,
COMMISSIONER

To,
The Principals/OSDs of all Govt. Polytechnics under control
of Department of Technical Education, Telangana State.

Copy to,
The Secretary, SBTET, T.S., Hyderabad for Information.
The Concerned officers in the O/o CTE & SBTET, TS, Hyderabad.
The concerned staff through Principal/OSD.

// T.C.F.B.O //


ASSISTANT DIRECTOR
Department of Technical Education
Government of Telangana, Hyderabad.
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ANNEXURE-I**SRUJANA TECH FEST - 2024-25 :: STATE LEVEL****Circuit Branch and Their Respective Branches**

Cluster.No	Name of the Circuit Branch	Branches in the Cluster*
Cluster-I	Civil Engineering & Allied Branches	AA, CE, MN
Cluster-II	Mechanical Engineering & Allied branches	AU, ME, MT
Cluster-III	Computer Engineering & Allied branches	AI, CCB, CPS, CS
Cluster-IV	Electrical Engineering & Allied branches	EE
Cluster-V	Electronics Engineering & Allied branches	BM, EC, EI, ES, EV
Cluster-VI	Special branches	CCP, HS, CH, LF, LG, PH, PK, PT, TT, TAN

***Note:**

AA - ARCHITECTURAL ASSISTANTSHIP	ES - EMBEDDED SYSTEMS
AI - ARTIFICIAL INTELLIGENCE & MACHINE LEARNING	EV - ELECTRONICS AND VIDEO ENGINEERING
AU - AUTOMOBILE ENGINEERING	HS - HOME SCIENCE
BM - BIOMEDICAL ENGINEERING	LF - LEATHER AND FASHION TECHNOLOGY
CCB - CLOUD COMPUTING AND BIG DATA	LG - LEATHER GOODS & FOOTWEAR TECHNOLOGY
CCP - COMMERCIAL AND COMPUTER PRACTICE	ME - MECHANICAL ENGINEERING
CE - CIVIL ENGINEERING	MN - MINING ENGINEERING
CH - CHEMICAL ENGINEERING	MT - METALLURGICAL ENGINEERING
CPS - CYBER PHYSICAL SYSTEMS AND SECURITY	PH - PHARMACY
CS - COMPUTER ENGINEERING	PK - PACKAGING TECHNOLOGY
EC - ELECTRONICS & COMMUNICATION ENGG	PT - PRINTING TECHNOLOGY
EE - ELECTRICAL AND ELECTRONICS ENGINEERING	TT - TEXTILE TECHNOLOGY
EI - ELECTRONICS AND INSTRUMENTATION ENGG	TAN - TANNERY

ANNEXURE-II**SRUJANA TECH FEST - 2024-25 :: STATE LEVEL**

The list of the State Level Coordinators and Assistant Coordinators for each Circuit Branch appointed by the Commissioner of Technical Education, TS, for conducting State Level Srujana Tech Fest 2024-25.

Cluster No.	Name of the Circuit Branch	Venue	Chairperson (Sarvasri)	Coordinator (Sarvasri)	Asst. Coordinator (Sarvasri)
Cluster-I	Civil Engineering & Allied Branches	639 - GPW, Secunderabad	K. Vinod Kumar, OSD	K. Naveen Kumar, HOD/CE	K. Sridevi, SL/CE
Cluster-II	Mechanical Engineering & Allied branches	002 - JNGPT, Ramanthapur	A. Vinay Kumar, Principal	P.Vinod Kumar, HOD/ME	K. Anjaneyulu, SL/ME
Cluster-III	Computer Engineering & Allied branches	054 - GIOE, Secunderabad	P. Annapurna, Principal	M.V.Sateesh Kumar, HOD/ECE	P. Srikar, L/CS
Cluster-IV	Electrical Engineering & Allied branches	001 - GPT, Hyderabad	Dr.K. Chandrashekar, Principal	N. Srinivas Rao, HOD/EE	P. Sravanthi, SL/EE
Cluster-V	Electronics Engineering & Allied branches	025 - SDDGWTTI, Hyderabad	M. Nagaraju, Principal	S. Jagadeesh Kumar, HOD/EC	K. Kiran Kumar, SL/EC
Cluster-VI	Special branches	001 - GPT, Hyderabad	Dr.K. Chandrashekar, Principal	G. Ravikanth Reddy, HOD/Pharmacy	L.Seetha SL/Chemistry

Sd/- A. Sridevasena,
COMMISSIONER


24/12/2025
ASSISTANT DIRECTOR
Department of Technical Education
Government of Telangana, Hyderabad.
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ANNEXURE - III**SRUJANA TECH FEST - 2024-25 :: STATE LEVEL****LIST OF PARTICIPANTS**

Name & Code of the Hosting Institution:

Name of the Circuit

Branch :

Cluster No:

S.No	Participating Institution & Code	Title of the Project	Details of the Participating Students			Name & Designation of the Guide	Remarks
			S.No.	Name	PIN		
			1				
			2				
			3				
			4				
			5				
			6				
			1				
			2				
			3				
			4				
			5				
			6				
			1				
			2				
			3				
			4				
			5				
			6				
			1				
			2				
			3				
			4				
			5				
			6				

Name & Signature of the State Level
Circuit Branch Co-ordinatorName & Signature of the
Hosting Principal/OSD

ANNEXURE - IV
SRUJANA TECH FEST - 2024-25 ::STATE LEVEL
EVALUATION SHEET

Name & Code of the Participant Institution:

Name of the Branch :

Circuit Branch :

Title of the Project :

S.No.	Evaluation Parameters	Max. Marks	Marks Awarded
1	Problem Statement: a. Relevance to real-world problem or need b. Clarity in defining the problem	5	
2	Innovation and Creativity: a. Originality of the idea b. Novelty in approach or technology used c. Creative problem-solving	15	
3	Technical Complexity: a. Depth of technical knowledge applied for design b. Use of advanced technologies or techniques or tools	20	
4	Practical Application: a. Potential impact of the project on society b. Feasibility of implementation & sustainability	10	
5	Prototype or Model or Demo: a. Quality & functionality of prototypes, models or software developed b. Performance, User friendliness and reliability	20	
6	Ethical Considerations: a. Safety of design, construction & operations b. Environmental Impact c. Legal/Copy Rights considerations	10	
7	Presentation: a. Presentation skills i.e. ability to explain the project effectively b. Quality of PPTs, Posters, visual aids, and demonstrations used c. Responses to Judges' questions	10	
8	Project Report/Documentation: a. Quality of the Report, including diagrams and user manuals b. Clarity, completeness, and organization of the Report	10	
Total		100	

Remarks of the Judge:

Name of the Judge

Designation

Signature

Note: Evaluation sheet should be given to each Judge for each project

ANNEXURE - V

SRUJANA TECH FEST - 2024-25 :: STATE LEVEL

CONSOLIDATED MERIT LIST

Name & Code of the Hosting Institution:

Name of the Circuit Branch:

Cluster No:

S.No.	Title of the Project	Details of the Participating Students			Name & Dsgn of the Guide & Institution	Avg.Marks Obtained*	Rank	Remarks
		S.No.	Name	PIN				
		1						
		2						
		3						
		4						
		5						
		6						
		1						
		2						
		3						
		4						
		5						
		6						
		1						
		2						
		3						
		4						
		5						
		6						
		1						
		2						
		3						
		4						
		5						
		6						

*The average marks of all the judges for 100

Name & Signatures of the Judges

Name & Signature of the
State Level Circuit Branch
Co-ordinatorName & Signature of the
Hosting Principal/VOSD

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ANNEXURE - VI**SRUJANA TECH FEST - 2024-25 :: STATE LEVEL****LIST OF WINNERS AT STATE LEVEL SRUJANA TECH FEST-2024-25****Name & Code of the Hosting****Institution:****Name of the Circuit Branch****Cluster No:****Cluster :**

S.No.	Name of the Institution & Code	Title of the Project	Details of the Participating Students			Name & Designation of the Guide	Remarks
			S.No.	Name	PIN		
1			1				I Prize
			2				
			3				
			4				
			5				
			6				
2			1				II Prize
			2				
			3				
			4				
			5				
			6				

Name & Signature of the State
Level Circuit Branch Co-ordinator

Name & Signature of the
Hosting Principal/OSD

Note: This Annexure should be submitted to the Asst. Director(T&P), O/o CTE, Hyd and a scanned copy should be sent to adtrg.ts@gmail.com

ANNEXURE - VII
SRUJANA TECH FEST - 2024-25 :: STATE LEVEL
FEEDBACK FORM

Name of the Hosting Institute:

Date:

Name of the Circuit Branch:

Cluster No:

1. Please give your overall opinion about the event.

2. Please give suggestions for improvement, if any.

(Signature & Name of the
Judge/Mentor/Participant)